Rev. 0.9 : Allegro.comm using ES2828. Updated 12/16/99

ES1988
Allegro.Comm

	Mother board design
Vaux supported	Remove : R46, R110, R100, Install : R111, R101
Non-Vaux	Remove : R111, R101 Install : R46, R100

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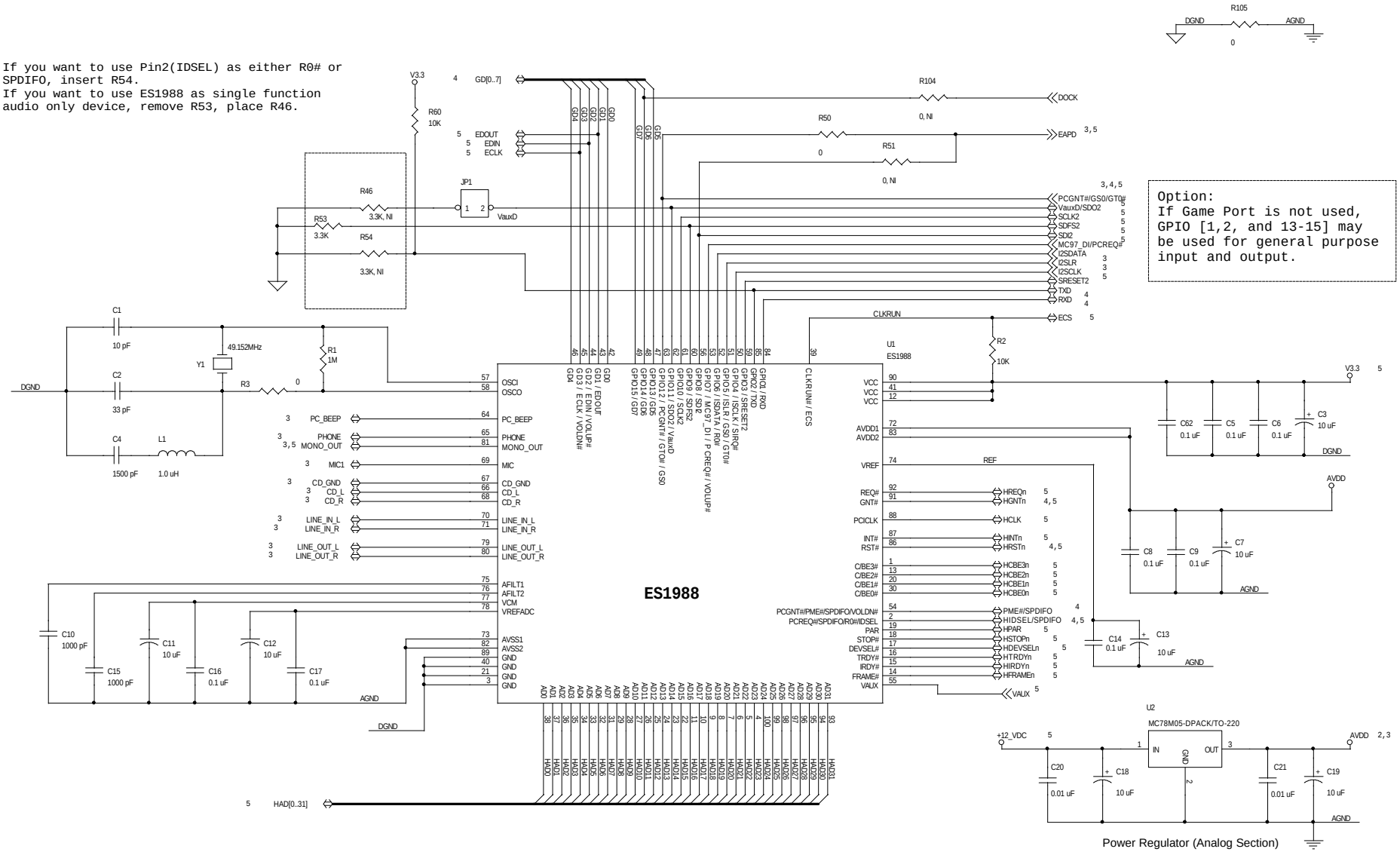
Title: History

Size | Document Number: Allegro.Comm Reference Board | Rev: 0.9

Date: Thursday, December 16, 1999 | Sheet 1 of 7

If you want to use Pin2(IDSEL) as either R0# or SPDIFO, insert R54.
If you want to use ES1988 as single function audio only device, remove R53, place R46.



Power Regulator (Analog Section)

Option:
If Game Port is not used, GPIO [1,2, and 13-15] may be used for general purpose input and output.

NOTE
1. GPIO PINS CAN NOT BE MULTI-PURPOSE IN ONE IMPLEMENTATION, PLEASE CONTACT ESS FAEs FOR OTHER GPIO APPLICATIONS.
2. SUBJECT TO CHANGE WITHOUT NOTICE, PLEASE CONTACT ESS FAEs FOR MORE DETAILS.

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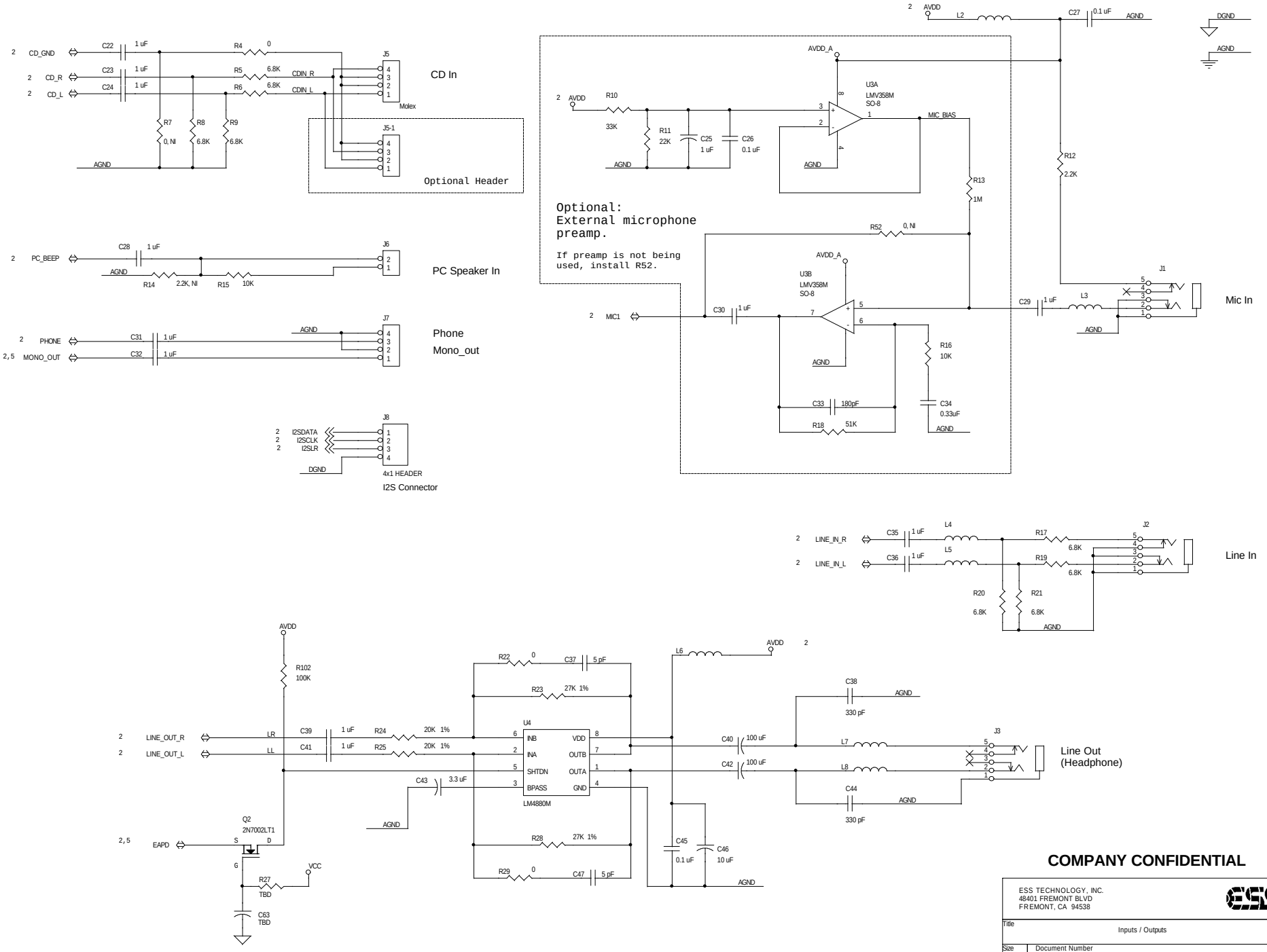
Title	ES1988	
Size	Document Number: Allegro.Comm Reference Board	Rev 0.9
Date: Tuesday, December 14, 1999	Sheet 2 of 7	



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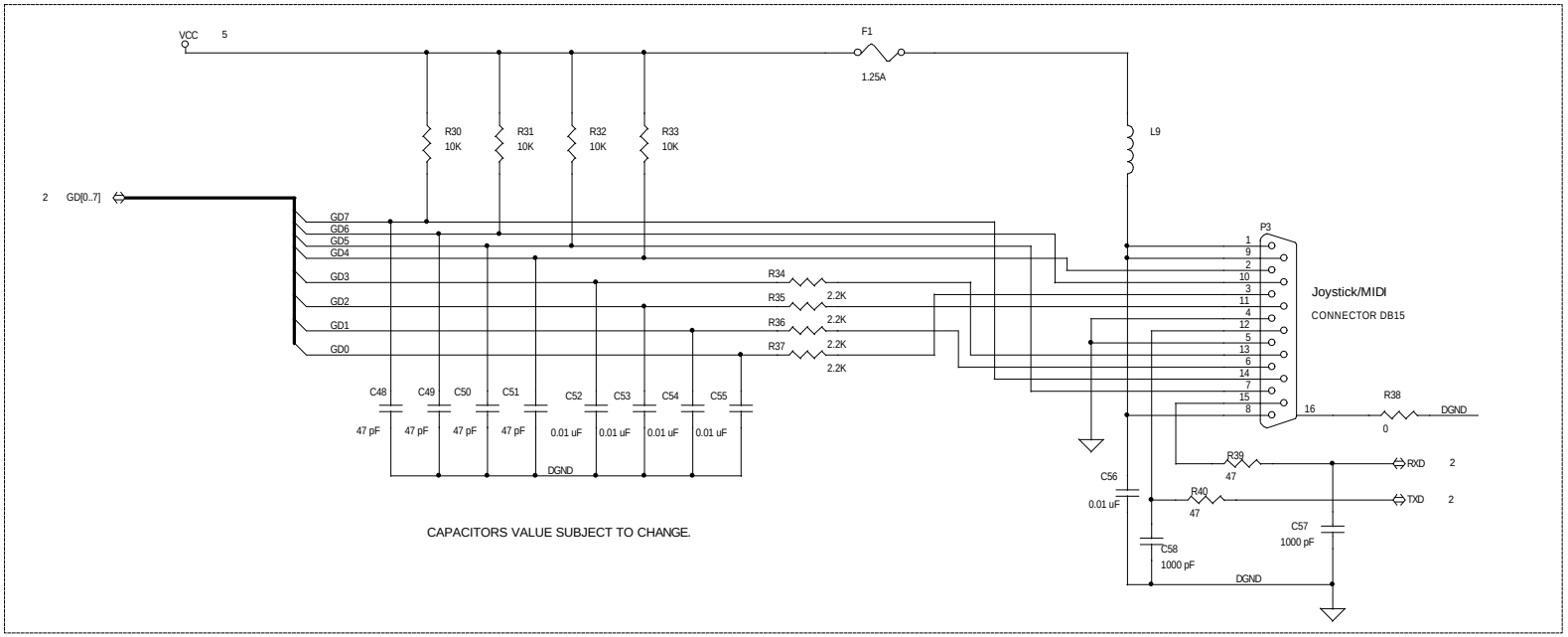
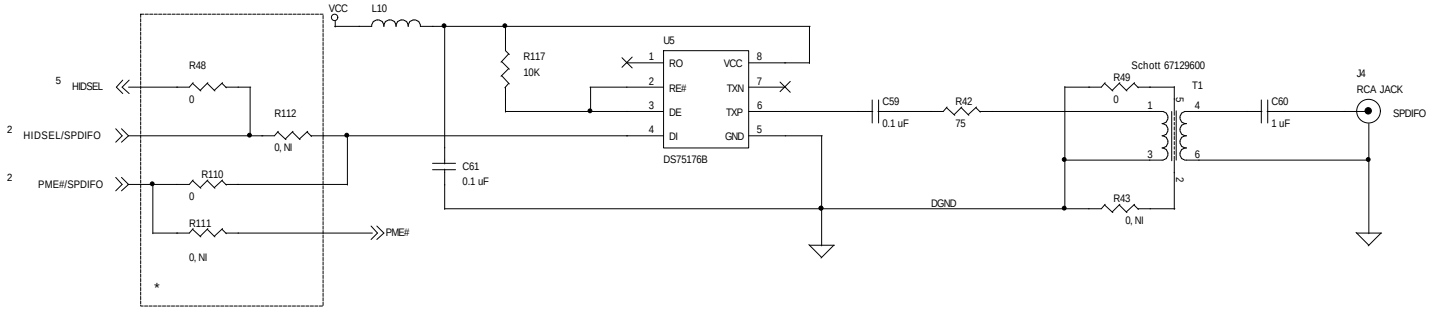
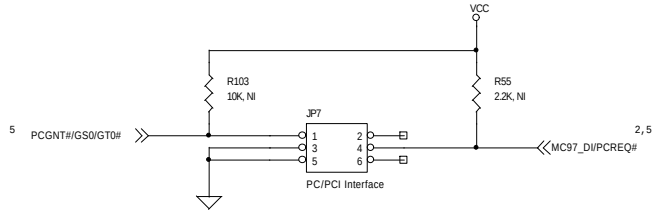
Title	Inputs / Outputs
Size	Document Number: Allegro.Comm Reference Board — Rev 0.9
Date: Friday, December 10, 1999	Sheet 3 of 7





* Note : To use pin 2 of ES1988 as a SPDIFO, install R112 and remove R48, R50.



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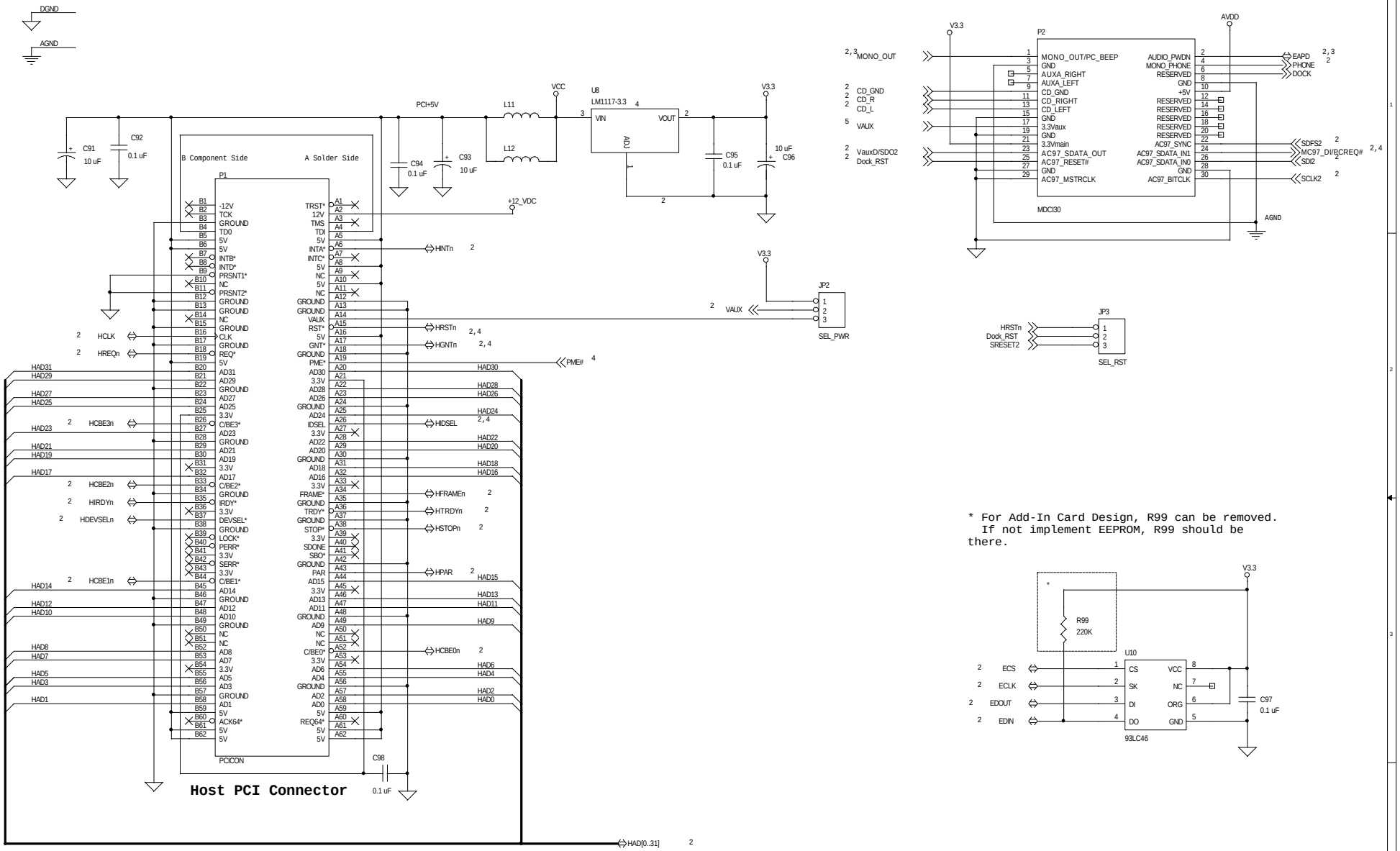
Title	Joysick / PCPCI / SPDIFO	
Size	Document Number: Allegro.Comm Reference Board	Rev 0.9
Date: Wednesday, December 15, 1999	Sheet 4 of 7	



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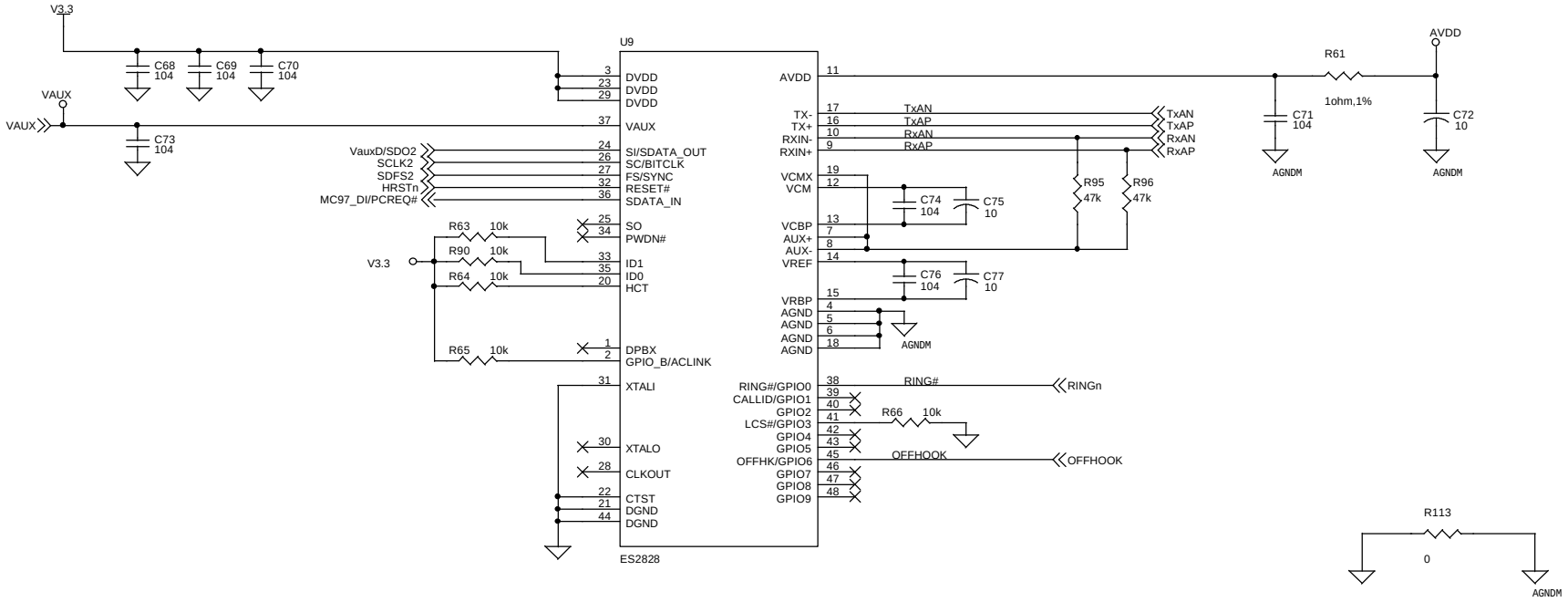
ESS TECHNOLOGY, INC. 48401 FREMONT BLVD FREMONT, CA 94538		
Title: PCI Conn / EEPROM / AC link		
Size	Document Number: Allegro.Comm Reference Board	Rev: 0.9
Date: Wednesday, December 15, 1999	Sheet 5 of 7	



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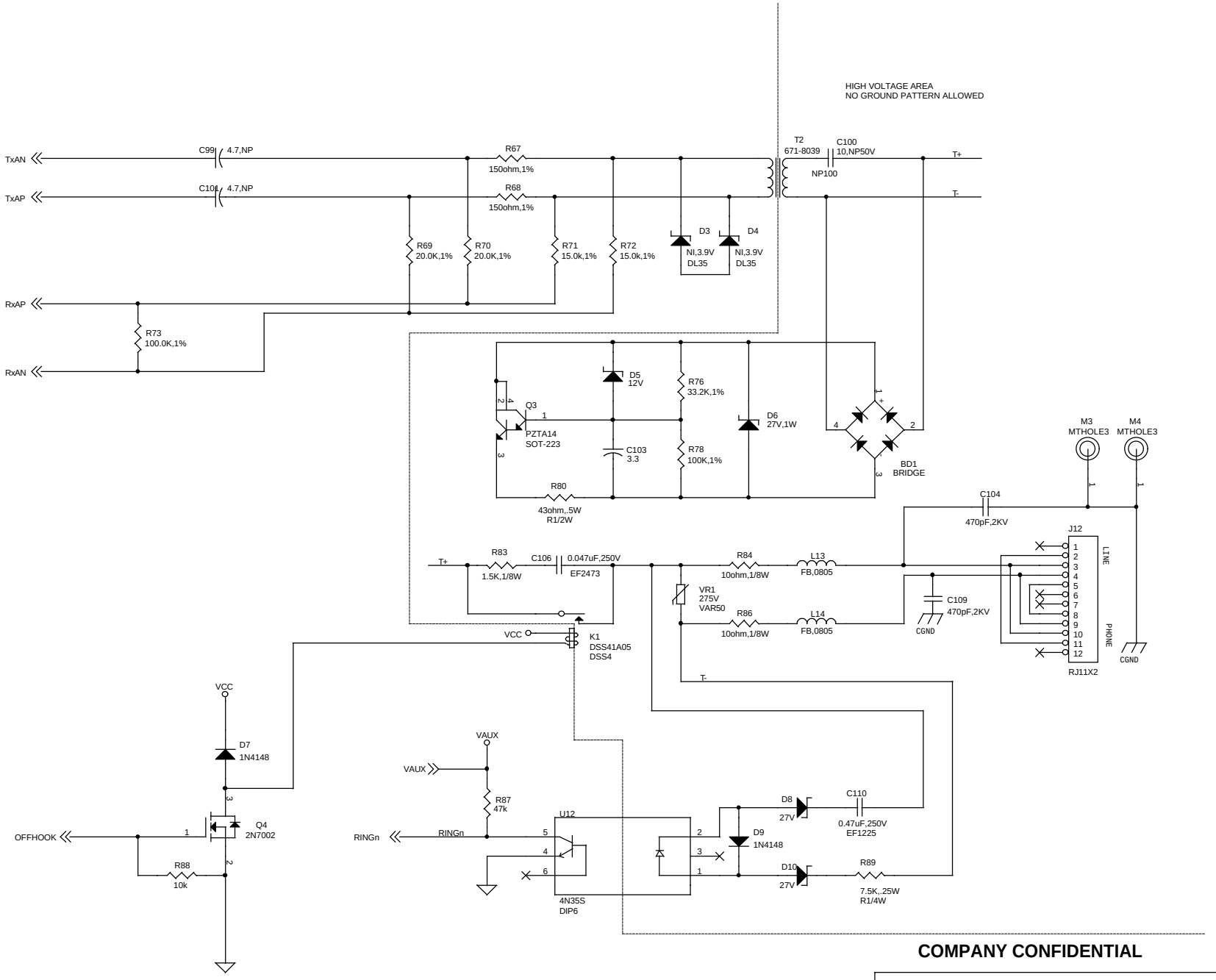
Title: MC97 (ES2828)		
Size	Document Number: Allegro.Comm Reference Board	Rev: 0.9
Date: Wednesday, December 15, 1999	Sheet 6 of 7	



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Title: DAA - US version		
Size	Document Number: Allegro.Comm Reference Board	Rev: 0.9
Date: Thursday, December 16, 1999	Sheet 7 of 7	